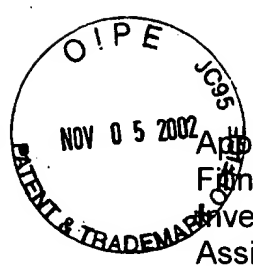


#30/Res
Jas
Kearns



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/517,127
Filing Date March 2, 2000
Inventor Scott E. Moore et al.
Assignee Micron Technology, Inc.
Group Art Unit 3723
Examiner T. Eley
Attorney's Docket No. MI22-1246
Title: "Semiconductor Processor Systems, A System Configured to Provide a
Semiconductor Workpiece Process Fluid (As Amended)"

PRELIMINARY AMENDMENT

To: Box RCE
Assistant Commissioner for Patents
Washington, D.C. 20231

EV182662774

From: James D. Shaurette (Tel. 509-624-4276; Fax 509-838-3424)
Wells, St. John, P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

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Sir:

Please enter the following amendments prior to examining the above identified application:

REMARKS

Claims 1-5, 7-29, 31-33, 39-65, 67, and 130-144 were indicated to be allowable in the parent application. This request for continued examination is being submitted out of an overabundance of caution to have the allowed claims considered in light of additional prior art. Such art is cited in an accompanying Information Disclosure Statement.